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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE  
PATENT OPERATION

Re the Application of: **ISHIOKA, Shogo et al.**

Group Art Unit: **2829**

Serial No.: **09/926,347**

Examiner: **HOLLINGTON, JERMELE M**

Filed: **October 17, 2001**

P.T.O. Confirmation No.: **7860**

For: **AN INSPECTION APPARATUS FOR CONDUCTIVE PATTERNS OF  
A CIRCUIT BOARD, AND A HOLDER THEREOF**

Attorney Docket No.: **011117**

Customer Number: **38834**

**RESUBMISSION OF CORRECTED FORMAL DRAWINGS**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, Virginia 22313-1450

Date: October 20, 2004

Sir:

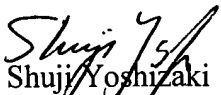
Applicants submit herewith 3 sheets of formal drawings depicting Figures 15-17 for the above-identified application.

Applicants respectfully request that the formal drawings submitted herewith be approved by the Examiner.

If any fees are required in connection with this submission, please charge our Deposit Account No. 50-2866.

Respectfully submitted,

WESTERMAN, HATTORI, DANIELS & ADRIAN, LLP

  
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Attachment: Limited Recognition  
Formal drawings (3 sheets; Figs 15-17)